|  |  |
| --- | --- |
| A | Actual Output |
| 0 |  |
| 1 |  |

11. Record the output’s value (0 or 1) in the first row of the NOT gate truth table in Figure 10.

*Figure 10*

8. Record the output’s value (0 or 1) in the first row of the AND gate truth table in Figure 14.

|  |  |  |
| --- | --- | --- |
| A | B | Actual Output |
| 0 | 0 |  |
| 0 | 1 |  |
| 1 | 0 |  |
| 1 | 1 |  |

*Figure 14*

8. Record the output’s value (0 or 1) in the first row of the OR gate truth table in Figure 20.

|  |  |  |
| --- | --- | --- |
| A | B | Actual Output |
| 0 | 0 |  |
| 0 | 1 |  |
| 1 | 0 |  |
| 1 | 1 |  |

*Figure 20*

9. Continue testing the other three combinations of inputs and outputs and recording the output values in the corresponding row of the truth table in Figure 20.

10. Verify that the outputs you obtained are the same as what you expected.

11. Disconnect power from the solderless breadboard.

12. Carefully remove the 74LS32.